

Serial No.: TBA  
Infineon Docket No.: 2002P50545US  
OC Docket No.: 1115747-0005

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants : Kiehl, et al.  
Serial No. : To Be Assigned  
Filed : June 26, 2003  
For : CURRENT SOURCE  
Examiner : To be assigned  
Group Art Unit : To be assigned

<p><b><u>Express Mail Label: EL 914767415US</u></b> I hereby certify that this paper is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. § 1.10 in an envelope addressed to: MS Patent Application Director of the US Patent and Trademark Office P.O. Box 1450 Alexandria, VA 22313-1450 on <b>June 26, 2003.</b> <u>BERNARD MAYS</u> Name of person mailing paper or fee <u>Bernard Mays</u> Signature of person mailing paper or fee</p>
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MS Patent Application  
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**INFORMATION DISCLOSURE STATEMENT  
WITHIN THREE MONTHS OF FILING OR  
BEFORE MAILING OF FIRST OFFICE ACTION (37 C.F.R. §1.97(b))**

Sir:

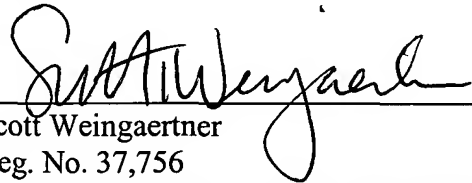
Applicant submits this communication in compliance with 37 C.F.R. §§1.56, 1.97 and 1.98. Attached to this communication is Form PTO-1449 identifying the publications for which consideration is requested.

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This Information Disclosure Statement is being filed concurrently with a new patent application and therefore no fee should be required. However, should it be determined that any fees are due in connection with this communication, authorization is hereby given to charge Deposit Account No. 23-1703.

Dated: June 26, 2003

Respectfully submitted,



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**Enclosures**

FORM PTO-1449 (REV. 2-82)  U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE  <b>INFORMATION DISCLOSURE STATEMENT  BY APPLICANT</b>	Atty. Docket No. 2002 P 50545 US	Serial No. To be assigned
	Applicants: Kiehl, et al.	
	Filing Date: June 26, 2003	Group: To be assigned

UNITED STATES PATENT DOCUMENTS

*Examiner Initial	Document No.							Date	Name	Class	Sub-Class	Filing Date, if Appropriate
	5	7	3	6	8	7	1	04/07/1998	Goto			
	5	3	8	1	1	1	3	01/10/1995	Kimura			

FOREIGN PATENT DOCUMENTS

*Examiner Initial	Document No.							Date	Country	Class	Sub-Class	Translation Yes No	

OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, Etc.)

		Xilinx, Virtex™-E 1.8 V Field Programmable Gate Arrays; Virtex-E Electrical
		Characteristics; DS022-3 (v2.7); pp. 1-25; February 1, 2002.

Examiner:

Date Considered:

\*Examiner: Initial citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.